Signal Processing First

Lecture 11
Linearity & Time-Invariance
Convolution

LECTURE OBJECTIVES

- GENERAL PROPERTIES of FILTERS
  - LINEARITY
  - TIME-INVARIANCE
  - ==> **CONVOLUTION**

LTI SYSTEMS

- BLOCK DIAGRAM REPRESENTATION
  - Components for Hardware
  - Connect Simple Filters Together to Build More Complicated Systems

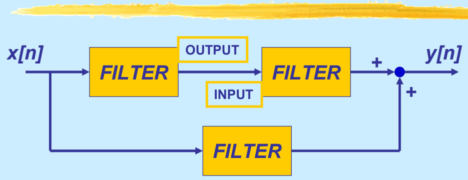
OVERVIEW

- IMPULSE RESPONSE, $h[n]$
  - FIR case: same as $\{b_k\}$
- CONVOLUTION
  - GENERAL: $y[n] = h[n] * x[n]$
  - GENERAL CLASS of SYSTEMS
  - LINEAR and TIME-INVARIANT
- ALL **LTI** systems have $h[n]$ & use convolution

DIGITAL FILTERING

- CONCENTRATE on the FILTER (DSP)
- DISCRETE-TIME SIGNALS
  - FUNCTIONS of n, the "time index"
  - INPUT $x[n]$
  - OUTPUT $y[n]$

BUILDING BLOCKS



- BUILD UP COMPLICATED FILTERS
  - FROM SIMPLE MODULES
  - Ex: FILTER MODULE MIGHT BE 3-pt FIR

GENERAL FIR FILTER

- FILTER COEFFICIENTS $\{b_k\}$
  - DEFINE THE FILTER

$$y[n] = \sum_{k=0}^{M} b_k x[n-k]$$

  - For example, $b_k = \{3, -1, 2, 1\}$

$$y[n] = \sum_{k=0}^{3} b_k x[n-k]$$
$$= 3x[n] - x[n-1] + 2x[n-2] + x[n-3]$$

MATLAB for FIR FILTER

- `yy = conv(bb,xx)`
  - VECTOR **bb** contains Filter Coefficients
  - SP-First: `yy = firfilt(bb,xx)`
- FILTER COEFFICIENTS $\{b_k\}$

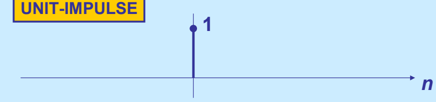
$$y[n] = \sum_{k=0}^{M} b_k x[n-k]$$

SPECIAL INPUT SIGNALS

- x[n] = SINUSOID **FREQUENCY RESPONSE**
- x[n] has only one NON-ZERO VALUE

$$\delta[n] = \begin{cases} 1 & n = 0 \\ 0 & n \neq 0 \end{cases}$$

UNIT-IMPULSE



FIR IMPULSE RESPONSE

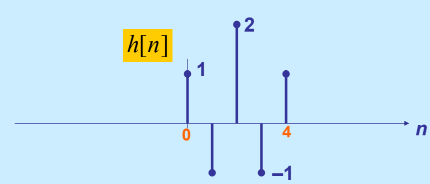
- Convolution = Filter Definition
  - Filter Coeffs = Impulse Response

n	$n<0$	0	1	2	3	...	M	$M+1$	$n>M+1$
$x[n]=\delta[n]$	0	1	0	0	0	0	0	0	0
$y[n]=h[n]$	0	b_0	b_1	b_2	b_3	...	b_M	0	0

$$h[n] = \sum_{k=0}^{M} b_k \delta[n-k]$$

MATH FORMULA for h[n]

- Use SHIFTED IMPULSES to write $h[n]$



LTI: Convolution Sum

- **Output = Convolution of x[n] & h[n]**
  - NOTATION: $y[n] = h[n] * x[n]$
  - Here is the FIR case:

FINITE LIMITS

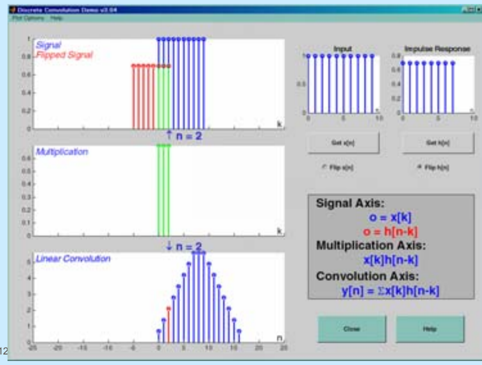
$$y[n] = \sum_{k=0}^{M} h[k] x[n-k]$$

Same as b_k **FINITE LIMITS**

CONVOLUTION Example

$$h[n] = \delta[n] - \delta[n-1] + 2\delta[n-2] - \delta[n-3] + \delta[n-4]$$
$$x[n] = u[n]$$

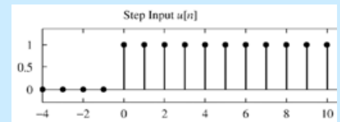
DCONVDEMO: MATLAB GUI



Turn to Your Neighbor

- FIR Filter is "FIRST DIFFERENCE"
  - $y[n] = x[n] - x[n-1]$
- INPUT is "UNIT STEP"

$$u[n] = \begin{cases} 1 & n \geq 0 \\ 0 & n < 0 \end{cases}$$



- Find $y[n]$

HARDWARE STRUCTURES

$x[n]$ → FILTER → $y[n]$

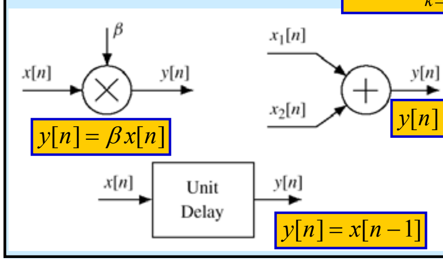
$$y[n] = \sum_{k=0}^{M} b_k x[n-k]$$

- INTERNAL STRUCTURE of "FILTER"
  - WHAT COMPONENTS ARE NEEDED?
  - HOW DO WE "HOOK" THEM TOGETHER?
- SIGNAL FLOW GRAPH NOTATION

HARDWARE ATOMS

- Add, Multiply & Store

$$y[n] = \sum_{k=0}^{M} b_k x[n-k]$$



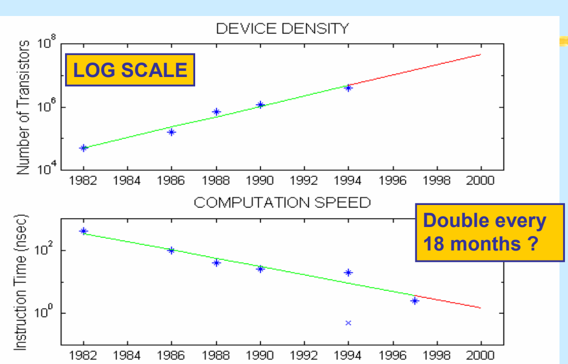
FIR STRUCTURE

- Direct Form

SIGNAL FLOW GRAPH

$$y[n] = \sum_{k=0}^{M} b_k x[n-k]$$

Moore's Law for TI DSPs



SYSTEM PROPERTIES

- MATHEMATICAL DESCRIPTION
- TIME-INVARIANCE
- LINEARITY
- CAUSALITY
  - “No output prior to input”

TIME-INVARIANCE

- IDEA:
  - “Time-Shifting the input will cause the same time-shift in the output”
- EQUIVALENTLY,
  - We can prove that
    - The time origin (n=0) is picked arbitrary

TESTING Time-Invariance

LINEAR SYSTEM

- LINEARITY = Two Properties
- SCALING
  - “Doubling x[n] will double y[n]”
- SUPERPOSITION:
  - “Adding two inputs gives an output that is the sum of the individual outputs”

TESTING LINEARITY

LTI SYSTEMS

- LTI: Linear & Time-Invariant
- COMPLETELY CHARACTERIZED by:
  - **IMPULSE RESPONSE** h[n]
  - **CONVOLUTION**: y[n] = x[n]*h[n]
    - The “rule”defining the system can ALWAYS be re-written as convolution
- FIR Example: h[n] is same as b_k

Turn to Your Neighbor

- FIR Filter is "FIRST DIFFERENCE"
  - $y[n] = x[n] - x[n-1]$
- Write output as a convolution
  - Need impulse response
  - Then, another way to compute the output:

CASCADE SYSTEMS

- Does the order of S_1 & S_2 matter?
  - NO, LTI SYSTEMS can be rearranged !!!
  - WHAT ARE THE FILTER COEFFS? $\{b_k\}$

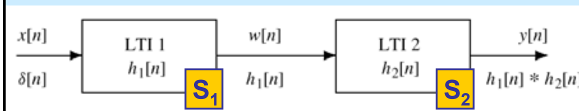


Figure 5.19 A Cascade of Two LTI Systems.

CASCADE EQUIVALENT

- Find "overall" h[n] for a cascade ?

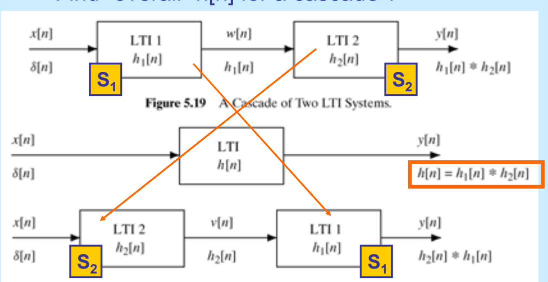


Figure 5.20 Switching the order of cascaded LTI systems.

One View of DSP, c. 1976

- "That discipline which has allowed us to replace a circuit previously composed of a capacitor and a resistor with two anti-aliasing filters, an A-to-D and a D-to-A converter, and a general purpose computer (or array processor) so long as the signal we are interested in does not vary too quickly."

Thomas P. Barnwell, III