

Module 10 Due the next class meeting after Module 10 is done in class.

- 1. Software pipeline the loop below. Show your dependency graph, scheduling table and final code including the prolog and epilog. Hint: You will need to use a Multi-Cycle Loop. Why?**

```
loop:
    ldh    *a_m++, a
    ldh    *b_m++, b
    add    a, b, c
    mpy    c, prod, prod

[i]      sub    i, 1, i
[i]      b      loop
```