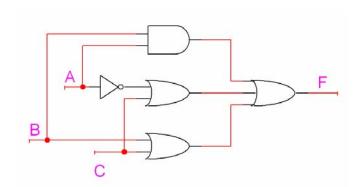
ECE130-01 Homework #2 (logic gate & truth table)

Fall 2003

1. Fill in the truth table and find the logic expression for the circuit below.



A	В	C	F
0	0	0	
0	0	1	
0	1	0	
0	1	1	
1	0	0	
1	0	1	
1	1	0	
1	1	1	

2. Complete the truth table and draw a logic circuit with AND, OR, and inverter gates for the following function: $F(A, B, C) = \overline{A} * B * \overline{C} + A * \overline{B}$.

A	В	C	F
0	0	0	
0	0	1	
0	1	0	
0	1	1	
1	0	0	
1	0	1	
1	1	0	
1	1	1	