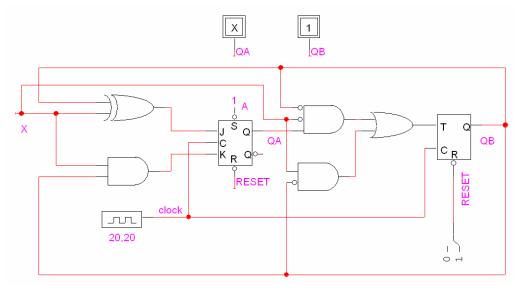
Name	CM Due date:	Tuesday, 14 Oct.
ECE130-1	Homework #16	Fall 2003
	(Sequential circuit with JK and T flip-flops)	

1 Obtain state equations, state table, and state diagram for the following circuit, where X is an input variable.



## **State equations**

Present	Next	
	X=0	X=1
QA QB	QA QB	QA QB
00		
01		
10		
11		





QA QB



