

Name _____

CM _____

Due date: Tuesday, Oct. 7

ECE130-03

Homework #12 (bus)

Fall 2003

- Construct an 8-bit subtractor circuit with two 74LS83 chips, 74LS04 chips, and busses to connect 8-bit inputs B7-0 and A7-0 respectively to proper chip inputs and to generate an eight bits difference D7-0 to be shown on two hex displays and a probe. The subtractor uses 2's complement method to carry out $A - B$ to produce D and Borrow. As much as possible, all connections should be made with busses.

Submit a paper copy of your solution with snapshots of sample subtractions and also email your solution to Mark.A.Yoder@rose-hulman.edu. Name your LogicWorks 4 file with hw12 followed by your last name, i.e., hw12yoder.

