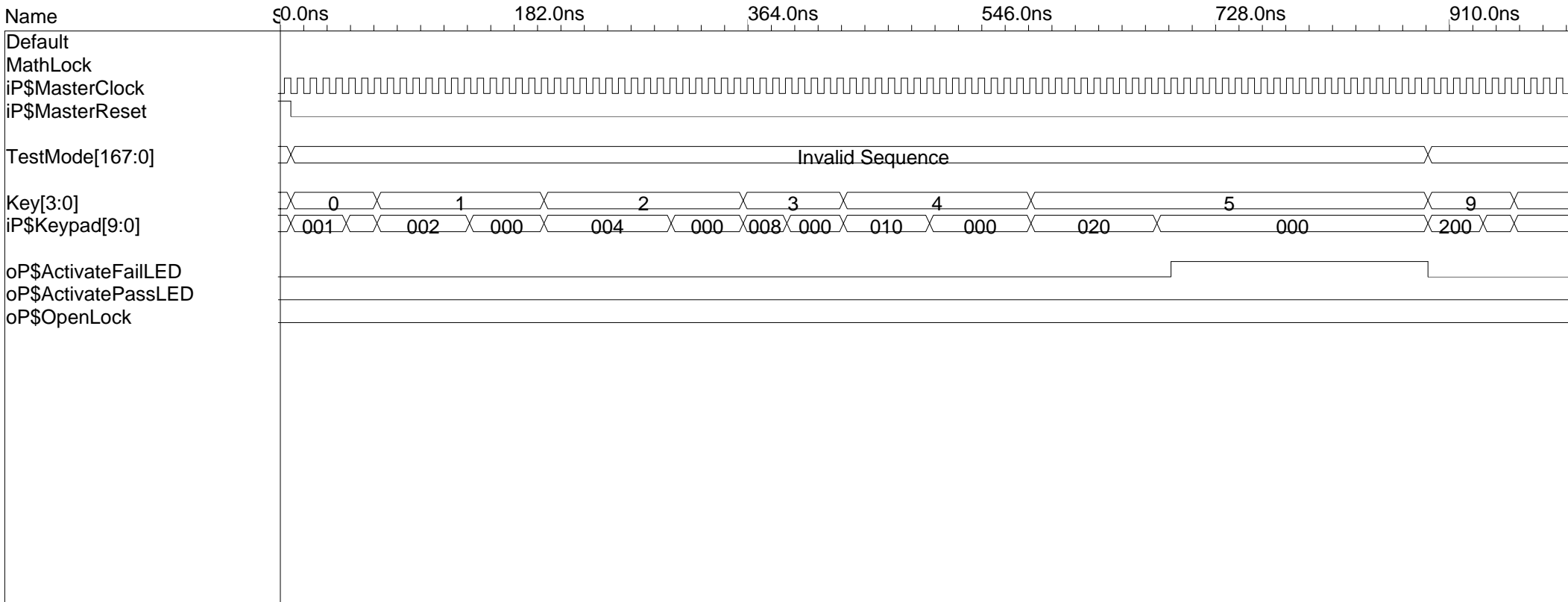


# MathLock

T1 1782.1ns T2 Tdelta



MathLock

T1 1782.1ns T2 Tdelta

