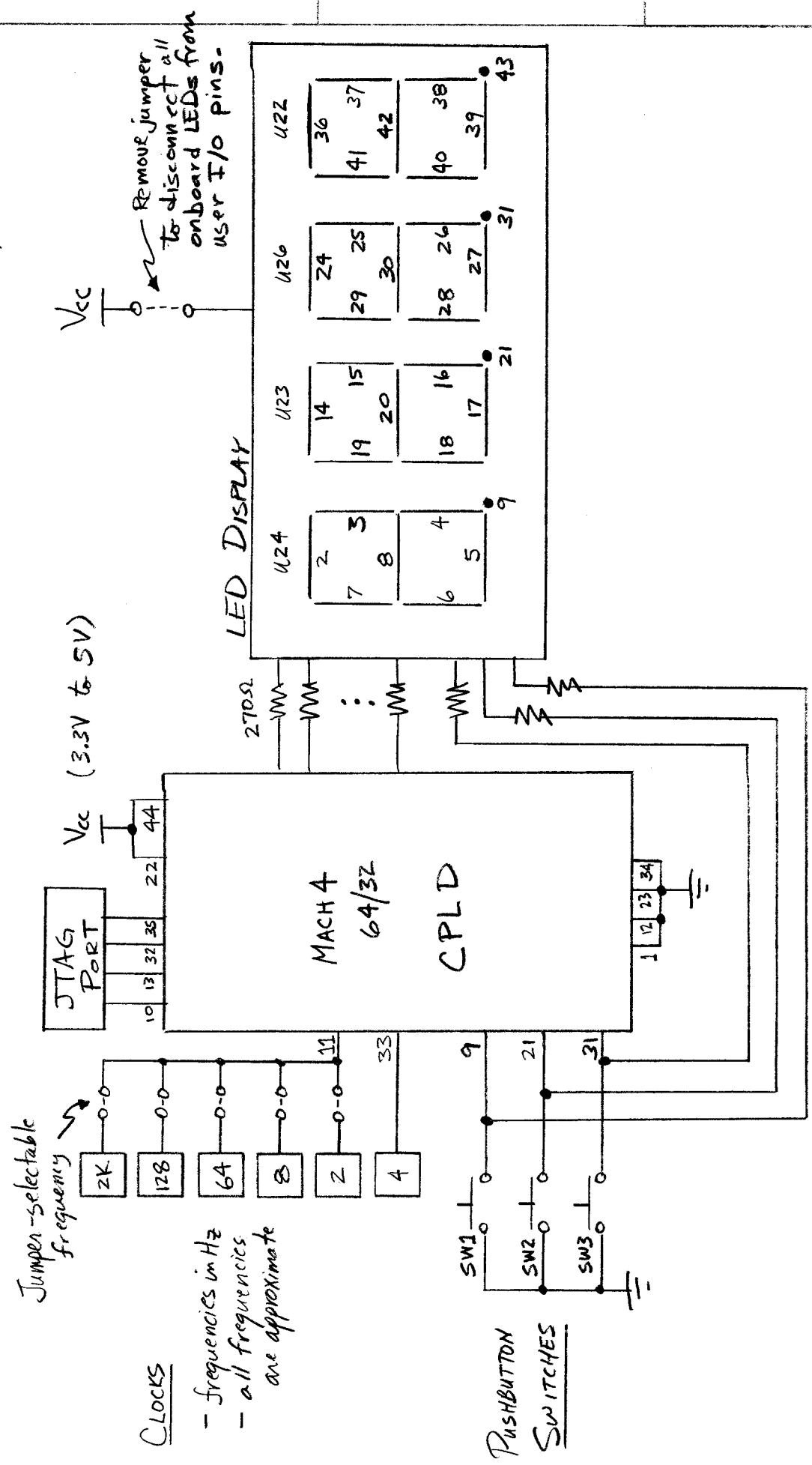


SIMPLIFIED SCHEMATIC for VANTIS EVALUATION BOARD

22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS

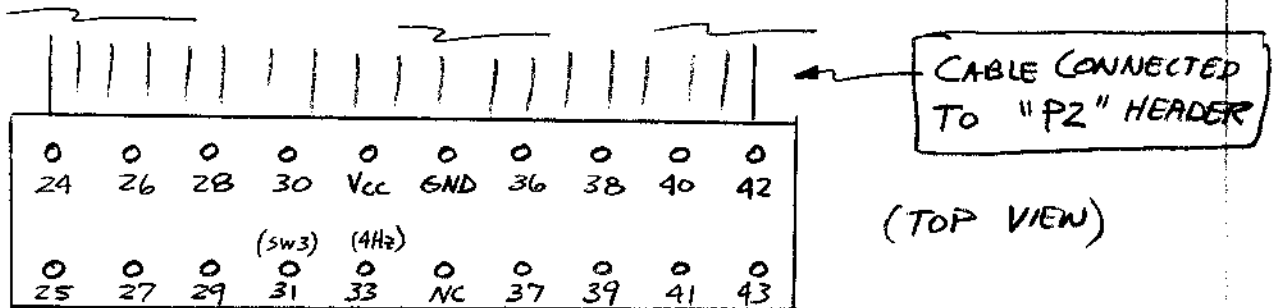
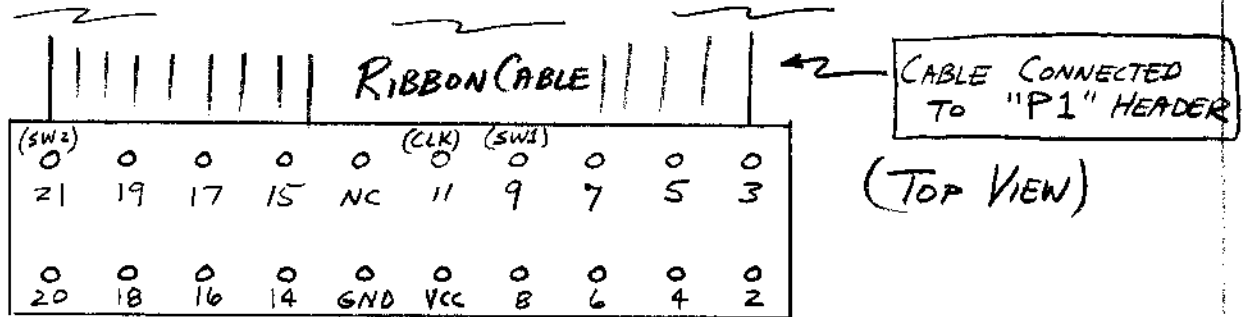
E. DEERING
1/10/2000



NOTES

- Switches & LEDs are active LOW.
- If pins 9, 21 or 31 are used as outputs, don't press the switch! (grounds the pin)
- Remove clock and LED jumpers to disconnect all user I/O pins from the onboard devices.
Pin 33 will still be connected to the 4Hz clock.

DIP PINOUTS for VANTIS CPLD BOARD RIBBON CABLES



NOTES

1. "NC" = no connect
 2. Numbers denote the pin numbers on the MACH4 CPLD
 3. Make sure you connect the "GND" (ground) pin to the system ground of your off-board circuit.
 4. The "Vcc" pin connects directly to the Vantis board's +5V DC power supply. You may use this pin to power your off-board circuit if your circuit does not require a great deal of current (the Vantis board power supply is rated for 225mA max)
- Do NOT connect another +5V source to the "Vcc" pin!
5. Ribbon cables exit from the right side of the Vantis board

50 SHEETS
 100 SHEETS
 22-141
 22-142
 22-143
 200 SHEETS

