

Name _____ Box _____ Due date: Monday, 11 Dec.

EC333

Homework #2

Winter 2000-2001

- 1 A particular logic family defines a LOW signal to be in the range of 0.0-1.2 V and a HIGH signal to be in the range of 3.8-5V. Under a positive-logic convention, indicate the logic value associated with each of the following signal levels:

(a) 1.0 V (b) 3 V (c) 4.2 V (d) 1.25 V
- 2 Find eight electrical parameters for MC74HC163 from the attached data sheet.
- 3 Find noise margins and “on” resistances of MC74HC163 from the attached data sheet. State assumptions required by your answer.
- 4 Find noise margins and fanouts of 74LS194 from the attached data sheet, State assumptions required by your answer.
- 5 Problem 3.23 on page 186 of Wakerly's book.