Rose-Hulman Institute of Technology
Electrical and Computer Engineering

ECE200 Course Schedule **Spring 2001**

Day	Date	Text	Topic	Problems
1-1	3/5	–	Introduction	1:1,6
1-2	6	1	Review of Electrical Systems	1:21,27
1-3	9	2.1–2	Topology, circuit analysis	2:1,3
2-1	12	2.2	Nodal analysis	2:4,6
2-2	13	2.3	Mesh analysis	2:17,19
2-3	16	2.4	More analysis	2:10,28
3-1	19	–	Exam No. 1	—
3-2	20	3.1–2	Linearity, proportionality	3:1,4
3-3	23	3.3	Superposition	3:5,6
4-1	26	–	Reciprocity, coupling	3:11,16
4-2	27	3.5	Source transformations, Thévenin & Norton	3:17,21
4-3	30	3.6	Thévenin's and Norton's Theorems	3:24,26
5-1	4/2	1,2,3	Exam No. 2	—
5-2	3	3.7	Matching	3:27,30
5-3	6	4.1–2	Design example	4:3,4
6-1	16	4.3–4	Design example	4:8,9
6-2	17	5.1–2	Time domain response	5:1,5
6-3	20	5.3–4	Transforming from time domain	5:6,15
7-1	23	5.5	Transforming circuits	5:21,25
7-2	24	5.6	Design example	5:27,30
7-3	27	6.1–2	Z(s) and H(s)	6:3,9
8-1	30	6.3	s-plane	6:19,23
8-2	5/1	6.4	Design example	6:25,29
8-3	4	7.1	s-plane and H(jω)	7:2,4
9-1	7	4,5,6	Exam No. 3	—
9-2	8	7.2	Sinusoidal steady-state response	7:5,7
9-3	11	7.3	Bode diagrams	7:10,14
10-1	14	7.4	Resonance	7:16,21
10-2	15	7.5	Design example	7:26,30
10-3	18	AOTA	Review	—
Final	21-24	AOTA	Final exam	—

Texts –Eccles, *Pragmatic Circuits*, Rose-Hulman 1999.

Reading – The reading matter for a given day is to be read *before* coming to class, and the classwork may assume that you have done so.

Instructor – E.R. Doering, CM110, Room C-211, Phone 8157, Ed.Doering@Rose-Hulman.Edu.

Homework – All work assigned is due in the format specified in *Guidelines and Standards for Writing Assignments* at the beginning of the period on each class day. Work assigned on one day is due the *next* class day unless an exam intervenes.