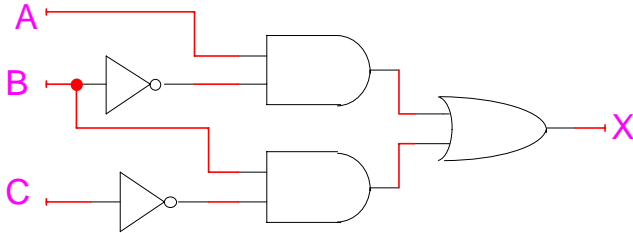


Name \_\_\_\_\_ Box \_\_\_\_\_

Due date: Thur. March 8

ECE130-02/03 Homework #2 (logic gate & truth table) Spring 2001

1. Fill in the truth table and find the logic expression for the circuit below.



A	B	C	X
0	0	0	
0	0	1	
0	1	0	
0	1	1	
1	0	0	
1	0	1	
1	1	0	
1	1	1	

2. Complete the truth table and draw a logic circuit with AND, OR, and inverter gates for the following function:  $F(A, B, C) = \bar{A} + B \cdot \bar{C}$ .

A	B	C	F
0	0	0	
0	0	1	
0	1	0	
0	1	1	
1	0	0	
1	0	1	
1	1	0	
1	1	1	