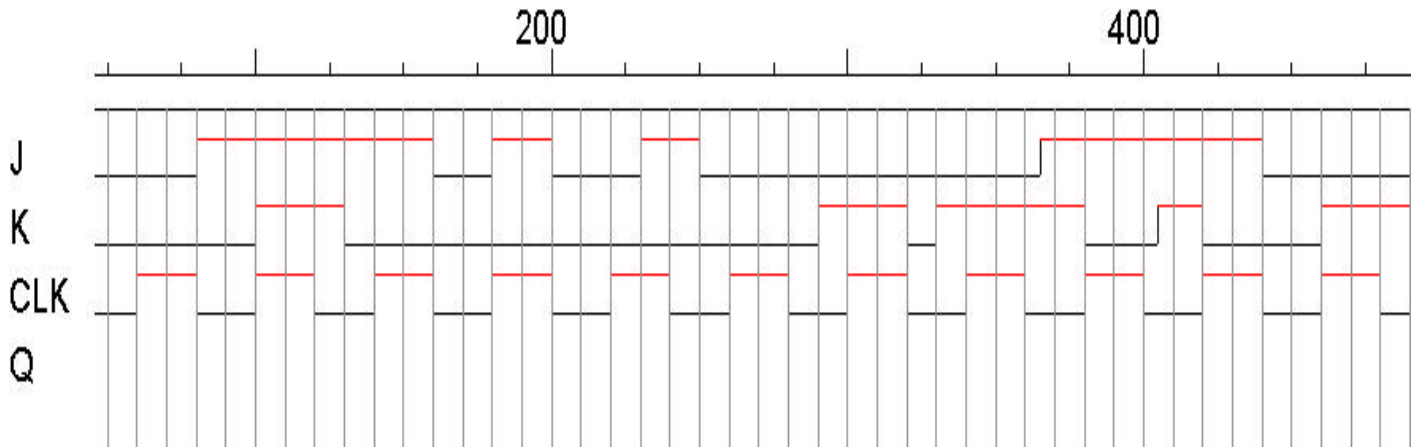


- 1 Draw the output waveform Q of a positive-edge triggered J-K flip-flop in the following timing diagram, assuming zero-delay time.



- 2 Draw the output waveform Q of a positive-edged triggered D flip-flop in the following timing diagram, assuming zero-delay time.

