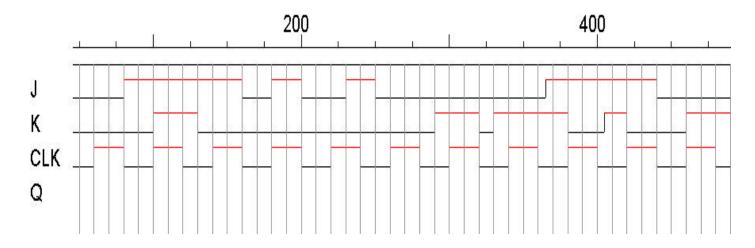
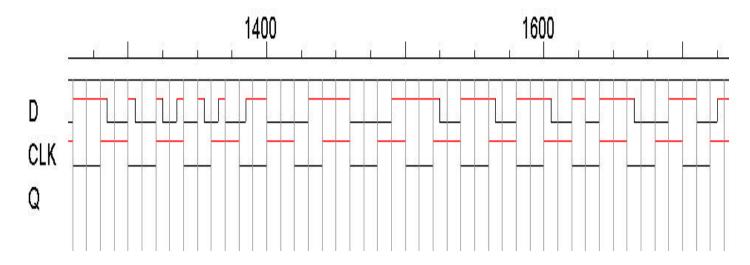
Name	Box	Due date: Tuesday April 17
ECE130	Homework #14	Spring 2001

1 Draw the output waveform Q of a positive-edge triggered J-K flip-flop in the following timing diagram, assuming zero-delay time.



2 Draw the output waveform Q of a positive-edged triggered D flip-flop in the following timing diagram, assuming zero-delay time.



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