

Instruction Selection via Tree- Pattern Matching

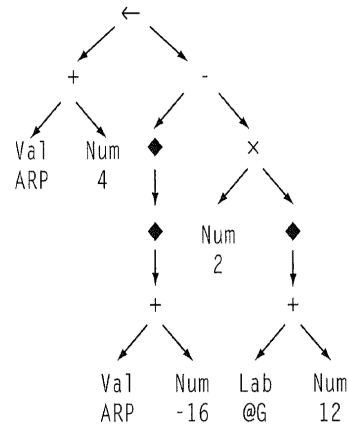
Objective

Annotate AST so as to produce close to optimal code.

Produce potential sequences of code and let the compiler schedule them at a later time.

This is an alternative to first generating code and then optimizing it through peep-hole optimization.

Annotated AST

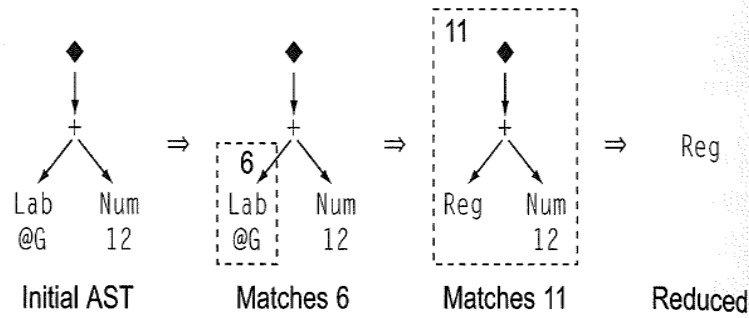


$$a = b - 2 * c$$

A diamond means an address
 Traverse tree and apply re-write rules

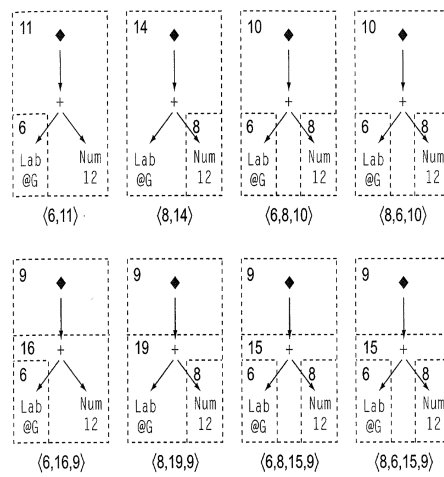
	Production	Cost	Code Template
1	Goal → Assign	0	
2	Assign → ← (Reg ₁ , Reg ₂)	1	store r ₂ ⇒ r ₁
3	Assign → ← (+ (Reg ₁ , Reg ₂), Reg ₃)	1	storeA0 r ₃ ⇒ r ₁ , r ₂
4	Assign → ← (+ (Reg ₁ , Num ₂), Reg ₃)	1	storeAI r ₃ ⇒ r ₁ , r ₂
5	Assign → ← (+ (Num ₁ , Reg ₂), Reg ₃)	1	storeAI r ₃ ⇒ r ₂ , r ₁
6	Reg → Lab ₁	1	loadI l ₁ ⇒ r _{new}
7	Reg → Val ₁	0	
8	Reg → Num ₁	1	loadI n ₁ ⇒ r _{new}
9	Reg → ◆ (Reg ₁)	1	load r ₁ ⇒ r _{new}
10	Reg → ◆ (+ (Reg ₁ , Reg ₂))	1	loadA0 r ₁ , r ₂ ⇒ r _{new}
11	Reg → ◆ (+ (Reg ₁ , Num ₂))	1	loadAI r ₁ , n ₂ ⇒ r _{new}
12	Reg → ◆ (+ (Num ₁ , Reg ₂))	1	loadAI r ₂ , n ₁ ⇒ r _{new}
13	Reg → ◆ (+ (Reg ₁ , Lab ₂))	1	loadAI r ₁ , l ₂ ⇒ r _{new}
14	Reg → ◆ (+ (Lab ₁ , Reg ₂))	1	loadAI r ₂ , l ₁ ⇒ r _{new}
15	Reg → + (Reg ₁ , Reg ₂)	1	add r ₁ , r ₂ ⇒ r _{new}
16	Reg → + (Reg ₁ , Num ₂)	1	addI r ₁ , n ₂ ⇒ r _{new}
17	Reg → + (Num ₁ , Reg ₂)	1	addI r ₂ , n ₁ ⇒ r _{new}
18	Reg → + (Reg ₁ , Lab ₂)	1	addI r ₁ , l ₂ ⇒ r _{new}
19	Reg → + (Lab ₁ , Reg ₂)	1	addI r ₂ , l ₁ ⇒ r _{new}
20	Reg → - (Reg ₁ , Reg ₂)	1	sub r ₁ , r ₂ ⇒ r _{new}
21	Reg → - (Reg ₁ , Num ₂)	1	subI r ₁ , n ₂ ⇒ r _{new}
22	Reg → - (Num ₁ , Reg ₂)	1	rsubI r ₂ , n ₁ ⇒ r _{new}
23	Reg → × (Reg ₁ , Reg ₂)	1	mult r ₁ , r ₂ ⇒ r _{new}
24	Reg → × (Reg ₁ , Num ₂)	1	multI r ₁ , n ₂ ⇒ r _{new}
25	Reg → × (Num ₁ , Reg ₂)	1	multI r ₂ , n ₁ ⇒ r _{new}

Example



■ FIGURE 11.5 A Simple Tree Rewrite Sequence.

But Wait! There is more!



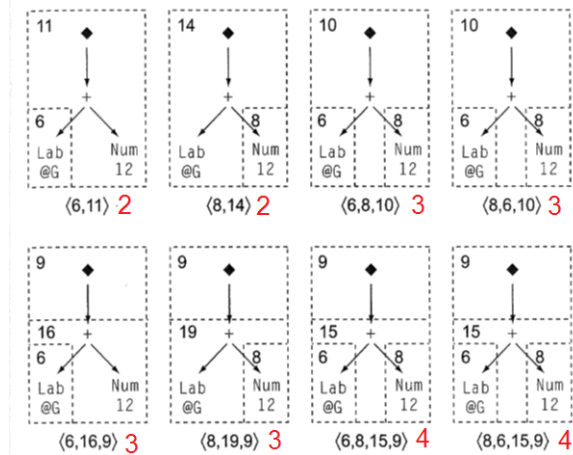


The Scream by Edvard Munch

Some Code to Calm you Down

<pre>loadI @G => r_i loadAI r_i,12 => r_j</pre>	<pre>loadI 12 => r_i loadAI r_i,@G => r_j</pre>	<pre>loadI @G => r_i loadI 12 => r_j loadAO r_i,r_j => r_k</pre>	<pre>loadI 12 => r_i loadI @G => r_j loadAO r_i,r_j => r_k</pre>
(6,11)	(8,14)	(6,8,10)	(8,6,10)
<pre>loadI @G => r_i addI r_i,12 => r_j load r_j => r_k</pre>	<pre>loadI 12 => r_i addI r_i,@G => r_j load r_j => r_k</pre>	<pre>loadI @G => r_i loadI 12 => r_j add r_i,r_j => r_k load r_k => r_l</pre>	<pre>loadI 12 => r_i loadI @G => r_j add r_i,r_j => r_k load r_k => r_l</pre>
(6,16,9)	(8,19,9)	(6,8,15,9)	(8,6,15,9)

Use Cost Function to Find Low-cost Solution



Simple Tree-walk Algorithm

```

Tile(n)
Label(n) ← ∅
if n is a binary node then
  Tile(left(n))
  Tile(right(n))
  for each rule r that matches n's operation
    if left(r) ∈ Label(left(n)) and right(r) ∈ Label(right(n))
      then Label(n) ← Label(n) ∪ {r}
else if n is a unary node then
  Tile(left(n))
  for each rule r that matches n's operation
    if left(r) ∈ Label(left(n))
      then Label(n) ← Label(n) ∪ {r}
else /* n is a leaf */
  Label(n) ← {all rules that match the operation in n}

```

Simple Tree-walk Algorithm

The algorithm labels each node with sets of patterns.

The compiler will can choose from them.

The algorithm uses a post-order traversal of the AST.

For assignment statements, generate code for rhs before lhs.